FORM PTO-1449 (SUBSTITUTE)	Attorney Docket No.: P2002,0542
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Appl. No.: Applicant: CHARLES SEELEY ET AL. Filing Date: June 26, 2003 Group Art Unit:

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
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/PFC/	J	02/31656 A2	4/18/02	WIPO				
/PFC/	K	54071954	6/8/79	Japan				
	L							
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

/PFC/		Author not listed: "Mechanism for Capturing Failure Data with a Replaceable Machine Element", IBM Technical Disclosure Bulletin, IBM Corp., Vol. 37, No. 01, January 1994, pp. 163-164
/PFC/		Kieckhafer, R. et al.: "The MAFT Architecture for Distributed Fault Tolerance", I.E.E.E. Transactions on Computers, IEEE, Vol. 37, No. 4, April 1988, pp. 398-405

EXAMINER	/Paul Contino/	DATE CONSIDERED	09/10/2007

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
/PFC/		Sachs, M. W.: "Automatic Transfer of Sense Data to Channel", IBM Technical Disclosure Bulletin, IBM Corp., Vol. 25, No. 3B, August 1982, pp. 1738-1739
/PFC/		Johnson, D. B.: "Error Reporting in the Intel iAPX 432", IEEE Comp. Soc. Press, 1984, pp. 24-28

EXAMINER	DATE CONSIDERED
/Paul Contino/	09/10/2007

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